

IN RE APPLICATION OF: James H. Lie

SERIAL NO.: 09/940,749

TITLE:



METHOD AND/OR ARCHITECTURE FOR GENERATING SUPERSET PINOUT FOR DEVICES WITH COMBINED PROGRAMMABLE LOGIC AND HIGH-SPEED SERIAL CHANNELS

FILED: August 28, 2001

EXAMINER: Thompson, A.

ART UNIT: 2825

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

**RESPONSE TRANSMITTAL AND
EXTENSION OF TIME REQUEST
(IF REQUIRED)**
FEE CALCULATION FOR ENCLOSED AND EXTENSION REQUEST (IF ANY)

	Claims Remaining	Highest No. Previous	Extra Rate	Additional Fee
Total Claims	20 minus	20 =	0 x \$ 18.00	\$ 0.00
Independent Claims	3 minus	3 =	0 x \$ 86.00	\$ 0.00
Multiple Dependent Claim First Added			+ \$290.00	\$ 0.00

TOTAL IF NOT SMALL ENTITY .. \$0.00

[] SMALL ENTITY STATUS - If applicable, divide by 2 \$0.00

[] Verified statement enclosed, if not previously filed.

[X] Applicant also requests a 2 month extension of time
for response to the outstanding Office Action. The fee is \$420.00

[X] Fee set forth in 37 C.F.R. 1.17 (b) for Notice of Appeal \$330.00

TOTAL FEE \$750.00

The Commissioner is hereby authorized to charge any overpayment or underpayment of the above fee associated with this Communication to Deposit Account No. 50-0541. A duplicate copy of this sheet is attached.

CHRISTOPHER P. MAIORANA, P.C.

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(586) 498-0670

By:

Robert M. Miller

Registration No.: 42,892

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on May 27, 2004.

By:

Jan M. Dunbar